

# Reserve Levels

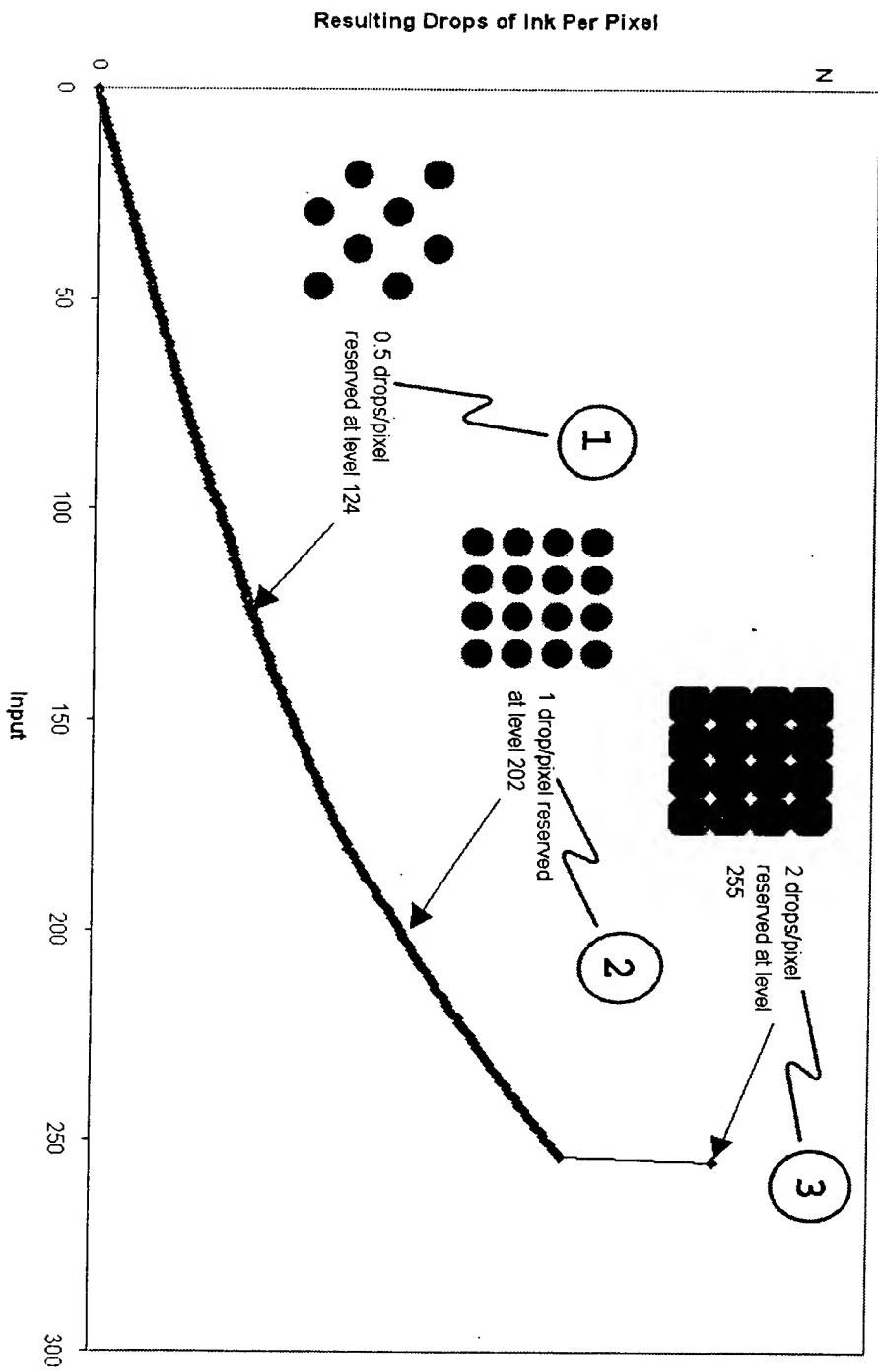


Figure 1